

257	738
Class	Subclass
ISSUE CLASSIFICATION	



U.S. UTILITY Patent Application

O.I.P.E. SCANNED <i>KSQ</i>	PATENT DATE
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APPLICATION NO. 09/856924	CONT/PRIOR D F	CLASS 400 257	SUBCLASS 738	ART UNIT 2826 2812	EXAMINER Tran, Tan
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APPLICANTS
Nobuaki Hashimoto

TITLE
Semiconductor device; method of manufacture thereof; circuit board and electronic device

PTO 2000
12/00

ISSUING CLASSIFICATION									
ORIGINAL			CROSS REFERENCE(S)						
CLASS	SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
[REDACTED]	738		25.7	668	676	688	694	737	734
INTERNATIONAL CLASSIFICATION:									
H	O	L	23.148						
			23.148.5						

Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drawn <u>20</u>	Figs. Drawn <u>21</u>	Print Figs. <u>5</u>	Total Claims <u>20</u>	Print Claim for O.G. <u>1</u>
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	<u>TAN RAN</u> (Assistant Examiner)			<u>07/03</u> (Date)	
<input checked="" type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____				NOTICE OF ALLOWANCE MAILED	
				ISSUE FEE	
				Amount Due	Date Paid
	_____ (Primary Examiner)			_____ (Date)	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner)			_____ (Date)	
				ISSUE BATCH NUMBER	

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